Title Mr.	First	SANDEEP	Last		RANA		
Designation		Name Name Assistant Professor					
					Vaal		
Dept. Name	Electronics & Communication Engineering						
Address:	ECE Department, G.B. Pant Institute of Engineering & Technology, Pauri Garhwal-246194						
Phone No.	999705	9997059553					
Email	1.srana2	1.srana231@gmail.com 2.					
Web Page (if any)							
Subjects Taught	UG Level: Network Analysis and Synthesis, Communication System, Digital Signal Processing, Analog Electronics, Electromagnetic Field Theory, Probability Theory & Stochastic Process, Multimedia Communication PG Level: Voice and Data Network, Advance Digital Signal Processing						
Areas of Interest/Specialization	Electronic Circuits for HF and Microwave frequency						
Experience (in years)	Total	07	07				
	Industry	0.5 y	0.5 year				
	Teachin	6.5 y	6.5 year				
	Researc	h					
Educational Qualifications	UG	G B.Tech.from Gr		aphi	c Era Univers	ity, Dehradun in 2012 with 82.3%	
	PG		M.Tech.from Delhi Technological University, New Delhi in 2014 with 80.5%				
	Doctora	te Pursu	Pursuing from IIT Roorkee				
	Any oth	ner					
Research Publications in Journals	1. M. Rani, K.S.Bhatia, H.Singh, H.Kaur, N. Gupta, <i>S.Rana</i> , "Synthesis and analof hexagonal ferrite for microwave absorption application in X-band", Journal of Optoelectronics and Advanced Materials vol. 24, No. 7-8, July – August 20: p. 402 – 409.						
	2. K. Kumawat, <i>S. Rana</i> , A. Sharma, "Design and Implementation of PID controller for second order plant and comparison of its performance with Fuzzy Logic based Controller", International Journal of Electronic and Electrical						

	Engineering (ISSN 0974-2174), Volume7, Number 1, pp.19-24, 2014.					
Papers Published in Conference Proceedings	 1.S. Rana, A. K. Kumawat and P. Kumar, "A Universal Current Mode KHN Biquad Filter using Voltage Differencing Current Conveyor," IEEE International Conference on Power Electronics, Intelligent Control and Energy Systems (ICPEICES), p.1-5, 2016 DOI: 10.1109/ICPEICES.2016.7853506 2. S. Rana, A. K. Kumawat and P. Kumar, "Dual output Voltage Differencing Buffered Amplifier based Wave Active filter," IEEE UP Section I nternational Conference on Electrical Computer and Electronics (UPCON), p.1-6,2015 DOI: 10.1109/UPCON.2015.7456736 					
Books Authored/Book Volume Chapters	None					
No. of Conferences	National	Attended	Organized			
		None	None			
	International	02	02			
Research Guidance	Awarded	PG	Doctorate			
		None	None			
	Undergoing	None	None			
Research Projects	Completed None					
	Undergoing	None				
Awards & Distinctions	Awarded with Best Paper Award for the research paper titled " Dual output Voltage Differencing Buffered Amplifier based Wave Active filter ," in IEEE UP Section International Conference on Electrical Computer and Electronics (UPCON-2015) held at Indian Institute of Information Technology Allahabad (IIIT-A), India.					
Administrative Assignments Handled	 Institute NIRF committee member Student Brach Advisor of IEEE student Branch Chapter. Member of Departmental BOS committee Department GATE committee coordinator Advance Simulation Lab incharge (OiC) Member of various "Josh-2019 (An annual Sport program) Committees 					
Association with Professional Bodies	1. Member of IEEE					
Any other Achievements	 Qualified ESE 2018 (Mains) and ESE 2019 (Prelims) conducted by UPSC. Qualified GATE ECE 2018, 2019 with 99.4% ile and 99.1% ile respectively. AIR 2 in Rajasthan Rajya Vidhyut Utpadan Nigam Limited recruitment exam for Engineer (Control & Instrumentation). Qualified ISRO Scientist/Engineer 'SC' 2017 written examination. Qualified UGC NET (for JRF and Assistant Professor both) in Electronic Science 					

held on June 2015.