

NITISH RAWAT



nitish.rawat1988@gmail.com ,9410124900

Educational Qualification:

Graduation:

- B.Tech from Uttarakhand Technical University, Dehradun in year 2011 with a percentage secured as 65.04%
- M.Tech from Graphic Era University, Dehradun in year 2013 with a percentage secured as 68.14%

Projects:

- **B.Tech:**

Title: “Input and Output Timing Device for the protection of Transformer”

In this project, we have to protect the electrical product (i.e. transformer) by fusing from under voltage and over voltage variations or fluctuations. For this, we have used an IC 555 as a timer circuit in both the sides i.e. input as well as output port to avoid damaging of device.

- **M.Tech:**

Minor Project:

Title: “Active and Reactive Power Flow Control using FACTS device”

In this project, we control the power flow (i.e. active and reactive) with the help of simulation of location of FACTS device using IEEE 5 bus system in which the FACTS device is placed at the mid-point of AC transmission line system. The FACTS device which is taken as: UPFC. Here, UPFC is also checked for the results.

Major Project:

Title: “Positioning of FACTS device in AC Transmission Line System”

In this project, we design an AC transmission line system with the use of FACTS device and then determine the exact location of FACTS device using simulation which can control the parameters (i.e. active and reactive power flow control) in power system. The FACTS device which is consider here: STATCOM. Simulation is done on IEEE 3 bus system for results. Also, STATCOM is analyzed for performance.

Publications:

1. Nitish Rawat, Ashutosh Bhatt and Pankaj Aswal, “**A review on optimal location of FACTS devices in AC transmission system**”,IEEE Conference on Power, Energy and Control, 6-8 Feb, 2013, Dindigul, Tamilnadu, India(Published).